

#21DS
10-22-02
PTO/US

Electronic Information Disclosure Statement

METHOD AND CIRCUIT FOR OPTIMIZING POWER CONSUMPTION IN A FLIP-FLOP

JC474 U.S. PRO
10/065226
09/26/02

Application:
Confirmation:
Applicant(s): ANTHONY CORREALE
Docket Number: RPS920020084
Group Art Unit:
Examiner:
search string: (5378934 or 4806786 or 4686394 or 5926487).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

Init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
XW	P01	5378934	1995-01-03		TAKAHASHI ET AL	327	203
1	P02	4806786	1989-02-21		VALENTINE	307	279
	P03	4686394	1987-08-11		LAM	307	455
✓	P04	5926487	1999-07-20		CHAPPELL ET AL	371	22.31

Remarks

(Remarks are not for responding to an office action.)

Applicants submit herewith patents, publications or other information which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56. While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

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Examiner Name	Date
WELLS	4/4/03